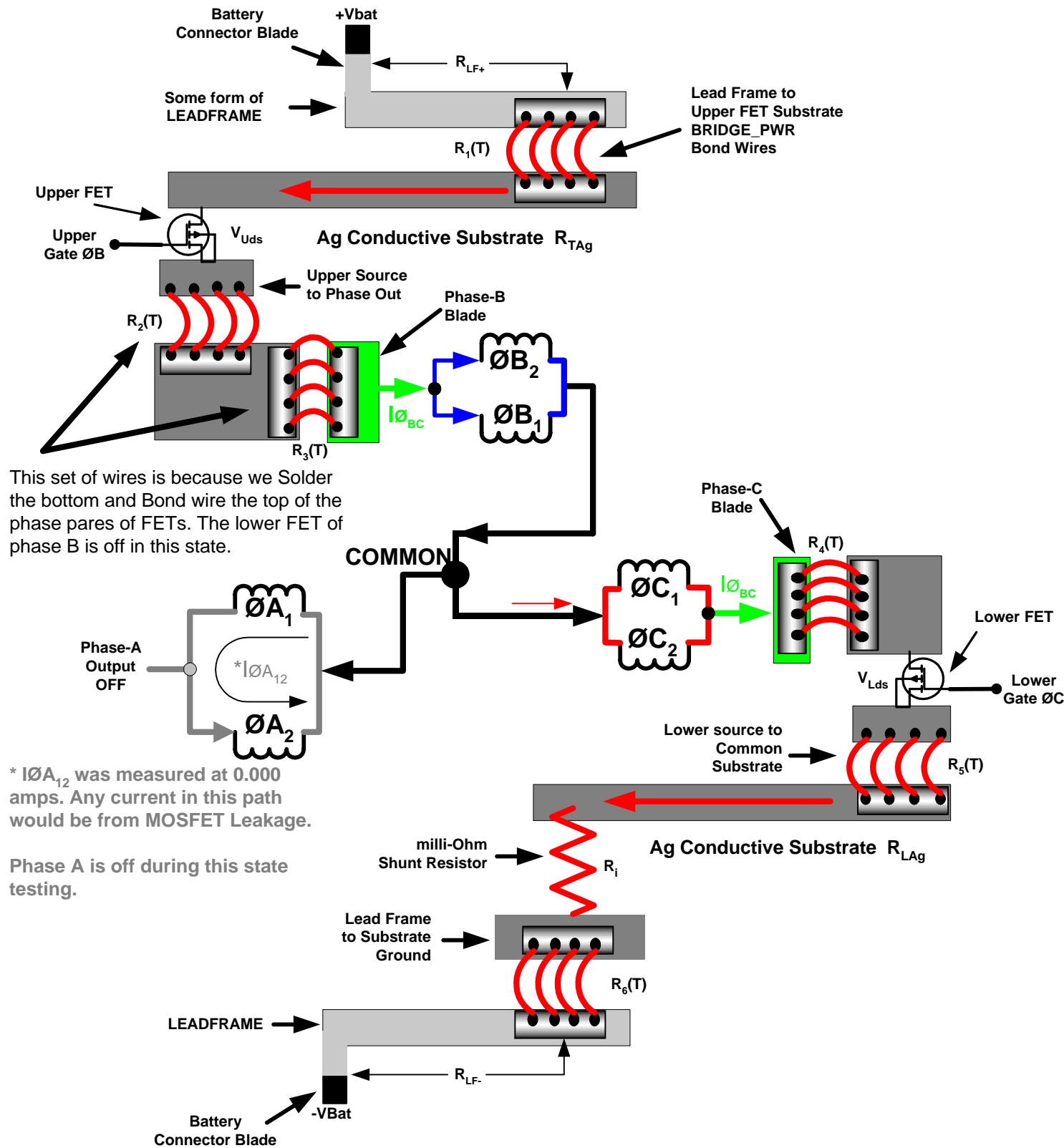


CURRENT CARRING CONDUCTORS IDENTIFICATION Phase to Phase - (B to C)

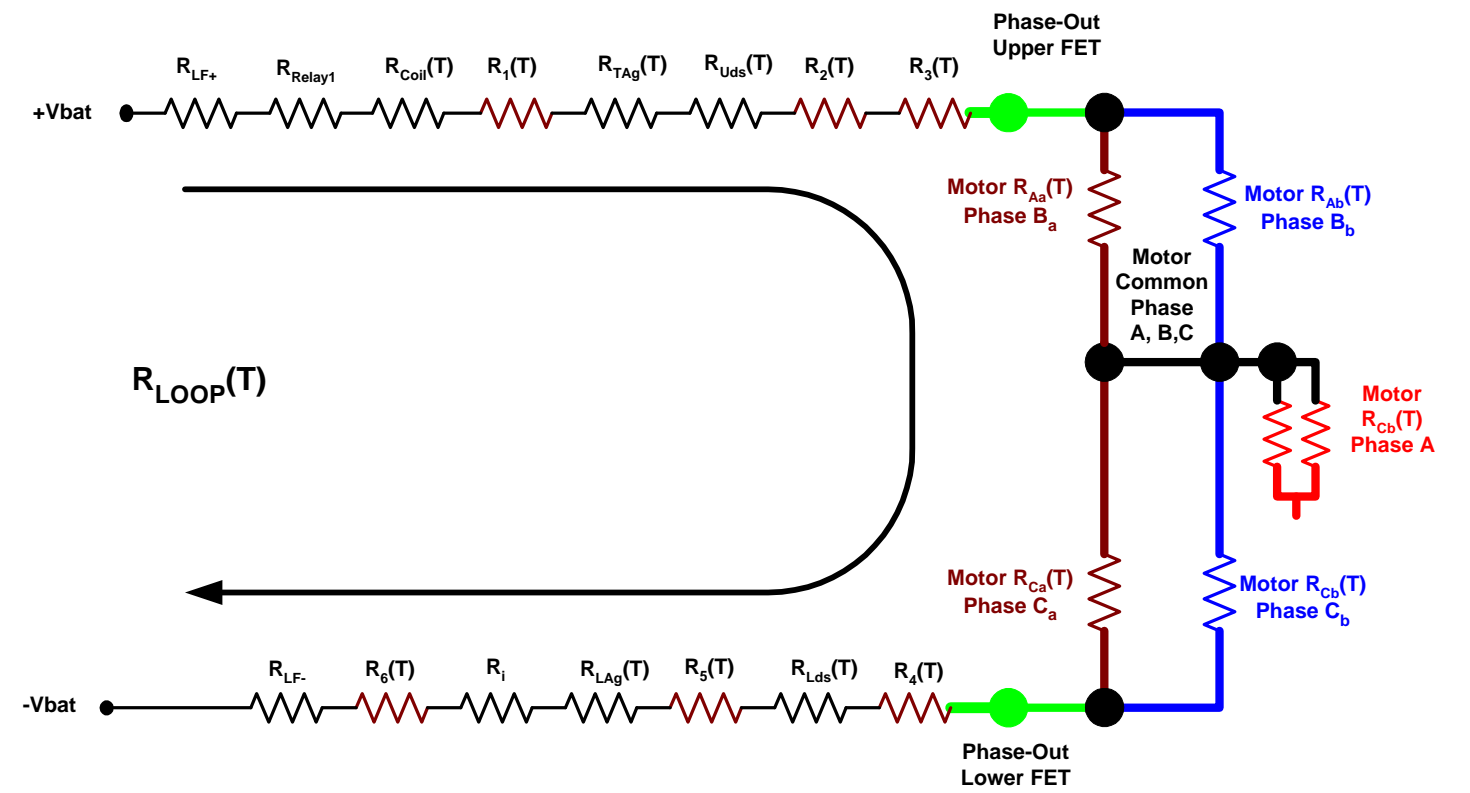


This set of wires is because we Solder the bottom and Bond wire the top of the phase pairs of FETs. The lower FET of phase B is off in this state.

* $I_{ØA_{12}}$ was measured at 0.000 amps. Any current in this path would be from MOSFET Leakage.

Phase A is off during this state testing.

Loop Resistance (DC) components from Phase B to Phase C for analysis. This shows all the components of the current conducting path.



CURRENT CARRYING CONDUCTORS IDENTIFICATION Single Phase B

